



Dr. Anugrah Jain
Assistant Professor
CSE, SVNIT, Surat

Specialization:

Advanced Computer Architecture, Network-on-Chip (NoC) Security, Routing in On-Chip Networks, Machine Learning for NoC, Internet of Things.

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Education:

- Ph. D. in Computer Science and Engineering from Malaviya National Institute of Technology Jaipur (MNIT Jaipur).
- M. Tech. in Computer Science & Engineering from Rajasthan Technical University Kota.
- B. Tech. in Computer Engineering from Rajasthan Technical University Kota.

Experience:

1. Assistant Professor, Department of Computer Science and Engineering, The LNM Institute of Information Technology, Jaipur, Rajasthan, India. (4 August 2021 – 12 January 2024)
2. Teaching Assistant, Department of Computer Science and Engineering, Sardar Vallabhbhai National Institute of Technology, Surat (SVNIT Surat), Gujarat, India. (25 August 2020 – 09 July 2021)

3. Guest Faculty, Department of Computer Science and Engineering, Government Engineering College Jhalawar, Rajasthan, India. (01 August 2014 - 29 May 2015)

Journal Publications:

1. **Anugrah Jain**, Vijay Laxmi, Manoj Singh Gaur and Ashish Sharma, "An improved reconfiguration algorithm for handling 1-point NoC failures", *Microprocessors and Microsystems (Elsevier)*: 104910, September 2023.
2. **Anugrah Jain**, Vijay Laxmi, Meenakshi Tripathi, Manoj Singh Gaur and Rimpay Bishnoi, "TRACK: An Algorithm for Fault-Tolerant, Dynamic and Scalable 2D Mesh Network-on-Chip Routing Reconfiguration", *Integration The VLSI Journal* 72: 92-110 (Elsevier), May 2020.
3. **Anugrah Jain**, Vijay Laxmi, Meenakshi Tripathi, Manoj Singh Gaur and Rimpay Bishnoi, "S2DIO: An Extended Scalable 2D Mesh Network-on-Chip Routing Reconfiguration for Efficient Bypass of Link Failures", *The Journal of Supercomputing* 75(10): 6855-6881 (Springer), October 2019.
4. **Anugrah Jain**, Nitin Purohit and Sushil Chandra Jain, "An extended approach for online testing of reversible circuits", *IOSR Journal of Computer Engineering* 1(16): 1-11, January 2014.

Papers in Conferences:

1. **Anugrah Jain**, Vijay Laxmi, Meenakshi Tripathi, Manoj Singh Gaur and Rimpay Bishnoi, "Performance-Enhanced d^2 -LBDR for 2D Mesh Network-on-Chip", In Kaushik B., Dasgupta S., Singh V. (eds) *VLSI Design and Test. VDAT 2017, Communications in Computer and Information Science*, pages 313-323, vol 711, Springer, Singapore, June 2017.
2. **Anugrah Jain** and Sushil Chandra Jain, "Towards Implementation of Fault Tolerant Reversible Circuits", In 2013 1st International Conference on Emerging Trends and Applications in Computer Science, pages 86-91, IEEE, September 2013.

Book:

1. **Anugrah Jain** and Sushil Chandra Jain, "Fault Tolerant Synthesis of Reversible Circuits", LAP Lambert Academic Publishing, ISBN 978-3-659-67914-8, pages 1-104, 2015.

Other Activities:

1. Publicity Chair in 4th International Workshop on Multimedia Applications (IWMA) held at LNMIIT Jaipur between March 14-18, 2022.
2. Ph. D. admission committee member at LNMIIT Jaipur.
3. NAAC documentation collection and coordination committee member at LNMIIT Jaipur.