

# SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY

## Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I FIRST SEMESTER COMMUNICATION SYSTEM AT REGULAR EXAM HELD DURING NOV/DEC 2020

		1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC621 T								
		Credit	3.00	3.00	3.00	4.00	3.00	3.00								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	DIGITAL SATELLITE COMMUNICATION (ELECTIVE)						
Semester : 1 GEETU			BB	BC	BB	BC	CD	BC								
<b>P20EC001</b>			<b>19.00</b>	<b>19.00</b>	<b>7.00</b>	<b>7.00</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC617 T								
		Credit	3.00	3.00	3.00	4.00	3.00	3.00								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)						
Semester : 1 CHAUDHARY NIKHIL KUMAR DINESHKUMAR			AA	BC	AB	BB	CC	BB								
<b>P20EC002</b>			<b>19.00</b>	<b>19.00</b>	<b>8.00</b>	<b>8.00</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC613 T								
		Credit	3.00	3.00	3.00	4.00	3.00	3.00								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	EMBEDDED SYSTEMS (ELECTIVE)						
Semester : 1 NITISH PANDEY			BB	AB	BB	BB	BC	CC								
<b>P20EC003</b>			<b>19.00</b>	<b>19.00</b>	<b>7.68</b>	<b>7.68</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC621 T	EC629 T							
		Credit	3.00	3.00	3.00	4.00	3.00	3.00	3.00							
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL SATELLITE COMMUNICATION (ELECTIVE)	PROBABILITY AND RANDOM PROCESSES (ELECTIVE)						
Semester : 1 LOKHANDE CHETAN ASHOK			BB	CC	BB	BC	CD	CD								
<b>P20EC004</b>			<b>19.00</b>	<b>19.00</b>	<b>6.53</b>	<b>6.53</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC621 T	EC629 T							
		Credit	3.00	3.00	3.00	4.00	3.00	3.00	3.00							
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL SATELLITE COMMUNICATION (ELECTIVE)	PROBABILITY AND RANDOM PROCESSES (ELECTIVE)						
Semester : 1 JUNED GADAMPALLI			AA	BB	AB	BB	CC	CC								
<b>P20EC006</b>			<b>19.00</b>	<b>19.00</b>	<b>7.84</b>	<b>7.84</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC621 T	EC629 T							
		Credit	3.00	3.00	3.00	4.00	3.00	3.00	3.00							
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL SATELLITE COMMUNICATION (ELECTIVE)	DIGITAL SATELLITE COMMUNICATION (ELECTIVE)						
Semester : 1 DIVYA K A			AA	AA	AA	AB	AB	AA								
<b>P20EC007</b>			<b>19.00</b>	<b>19.00</b>	<b>9.63</b>	<b>9.63</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC621 T	EC629 T							
		Credit	3.00	3.00	3.00	4.00	3.00	3.00	3.00							
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	DIGITAL SATELLITE COMMUNICATION (ELECTIVE)						
Semester : 1 AJITH N			BC	CC	BB	BB	CD	BB								
<b>P20EC008</b>			<b>19.00</b>	<b>19.00</b>	<b>7.05</b>	<b>7.05</b>										

Abbreviations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10 AB-9 BB-8 BC-7 CC-6 CD-5 DD-4 FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFACTORY ATTENDANCE NP/NF-AUDIT PASS/FAIL

Date : 18-02-2021

The actual mark statement with the Academic / Examination section shall be final & binding to all

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**Ichchhanath, Surat-395007 (Gujarat), India**

**REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I FIRST SEMESTER COMMUNICATION SYSTEM AT REGULAR EXAM HELD DURING NOV/DEC 2020**

		1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC613 T								
		Credit	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	EMBEDDED SYSTEMS (ELECTIVE)						
Semester : 1 AMRUTHA R MENON			AB	BB	AB	AB	BC	BB								
<b>P20EC009</b>			<b>19.00</b>	<b>19.00</b>	<b>8.37</b>	<b>8.37</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC621 T								
		Credit	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	DIGITAL SATELLITE COMMUNICATION (ELECTIVE)						
Semester : 1 PASTULA PHANI			AB	BC	AB	AB	CC	BB								
<b>P20EC010</b>			<b>19.00</b>	<b>19.00</b>	<b>8.05</b>	<b>8.05</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC629 T								
		Credit	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	PROBABILITY AND RANDOM PROCESSES (ELECTIVE)						
Semester : 1 SOURABH MOHAN M V			BB	BC	BB	BB	CD	AB								
<b>P20EC011</b>			<b>19.00</b>	<b>19.00</b>	<b>7.53</b>	<b>7.53</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC613 T								
		Credit	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	EMBEDDED SYSTEMS (ELECTIVE)						
Semester : 1 METKAR SHREYA VIJAY			AB	BC	BB	BB	BC	CC								
<b>P20EC012</b>			<b>19.00</b>	<b>19.00</b>	<b>7.53</b>	<b>7.53</b>										
Semester	Student Name	Course Code	EC601 T	EC603 T	EC605 T	EC607 P	EC621 T	EC629 T								
		Credit	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL SATELLITE COMMUNICATION (ELECTIVE)	PROBABILITY AND RANDOM PROCESSES (ELECTIVE)						
Semester : 1 RAHUL KUMAR			BB	BC	BB	AB	CC	AB								
<b>P20EC013</b>			<b>19.00</b>	<b>19.00</b>	<b>7.89</b>	<b>7.89</b>										

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Date : 18-02-2021

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## Ichchhanath, Surat-395007 (Gujarat), India

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		1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester : 1 <b>P20EC014</b> Admission Number : Reg-Credit : Earn-Credit : SGPA : CGPA :	Student Name	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC613 T									
	Course Code	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>									
	Credit	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	EMBEDDED SYSTEMS (ELECTIVE)									
Semester : 1 UTKARSH SRIVASTAVA		AB	BB	AB	AB	BC	CC									
Semester : 1 <b>P20EC015</b> Admission Number : Reg-Credit : Earn-Credit : SGPA : CGPA :	Student Name	EC601 T	EC603 T	EC605 T	EC607 P	EC617 T	EC629 T									
	Course Code	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>									
	Credit	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	VLSI TECHNOLOGY (ELECTIVE)	PROBABILITY AND RANDOM PROCESSES (ELECTIVE)									
Semester : 1 HIMANSHI SONI		BB	BB	BB	BB	CC	BC									
Semester : 1 <b>P20EC017</b> Admission Number : Reg-Credit : Earn-Credit : SGPA : CGPA :	Student Name	EC601 T	EC603 T	EC605 T	EC607 P	EC617 T	EC629 T									
	Course Code	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>									
	Credit	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	VLSI TECHNOLOGY (ELECTIVE)	PROBABILITY AND RANDOM PROCESSES (ELECTIVE)									
Semester : 1 SHRADHA GUPTA		AB	AB	AB	BC	CC	CC									
Semester : 1 <b>P20EC018</b> Admission Number : Reg-Credit : Earn-Credit : SGPA : CGPA :	Student Name	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC621 T									
	Course Code	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>									
	Credit	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	DIGITAL SATELLITE COMMUNICATION (ELECTIVE)									
Semester : 1 PATNALA GANESH		AB	BB	BB	BB	BC	BB									
Semester : 1 <b>P20EC019</b> Admission Number : Reg-Credit : Earn-Credit : SGPA : CGPA :	Student Name	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC617 T									
	Course Code	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>									
	Credit	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 SUNKARI USHA		BB	CC	BB	BC	DD	BC									
Semester : 1 <b>P20EC020</b> Admission Number : Reg-Credit : Earn-Credit : SGPA : CGPA :	Student Name	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC617 T									
	Course Code	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>									
	Credit	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	VLSI TECHNOLOGY (ELECTIVE)									
Semester : 1 V CHAITANYA		AB	BB	AB	BB	BC	AB									
Semester : 1 <b>P20EC021</b> Admission Number : Reg-Credit : Earn-Credit : SGPA : CGPA :	Student Name	EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC629 T									
	Course Code	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>									
	Credit	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	PROBABILITY AND RANDOM PROCESSES (ELECTIVE)									
Semester : 1 PERMI BABA SAIRAM		BB	BC	BB	BC	DD	AB									

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**Ichchhanath, Surat-395007 (Gujarat), India**

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					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark	
Semester	Student Name	Course Code			EC601 T	EC603 T	EC605 T	EC607 P	EC609 T	EC613 T										
		Credit			<b>3.00</b>	<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>										
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	RF CIRCUITS & SYSTEMS	ADVANCED OPTICAL COMMUNICATION SYSTEMS	ADVANCE DSP	COMMUNICATION LAB - I	DIGITAL VLSI DESIGN (ELECTIVE)	EMBEDDED SYSTEMS (ELECTIVE)										
Semester : 1 MUGATI UDHAY KUMAR					AB	BC	BB	AB	CD	BC										
<b>P20EC022</b>					<b>19.00</b>	<b>19.00</b>	<b>7.58</b>	<b>7.58</b>												

Total Student Count : 20