

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY
Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT B.TECH. IV SEVENTH SEMESTER ELECTRONICS & COMMUNICATION ENGINEERING AT REGULAR EXAM HELD DURING NOV/DEC 2020

					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name	Course Code			EC401 T	EC401 P	EC403 T	EC403 P	EC405 T	EC405 P	EC413 T	EC407 P	EC409 P	EC203 T					
		Credit			4.00	1.00	4.00	1.00	3.00	1.00	3.00	2.00	2.00	4.00					
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	VLSI DESIGN	VLSI DESIGN (PRAC.)	MOBILE COMMUNICATION	MOBILE COMMUNICATION (PRAC.)	ELECTRONICS INSTRUMENTATION	ELECTRONICS INSTRUMENTATION (PRACT.)	ADAPTIVE SIGNAL PROCESSING (ES-I)	SEMINAR	PROJECT PRELIMINARIES	DIGITAL LOGIC DESIGN					
Semester : 7 PARSHA RAVI TEJA					BC	BB	CC	CD	BB	AA	BB	BB	BB	BC					
U17EC098					25.00	25.00	7.32	5.53											

Total Student Count : 1