



डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग
DEPARTMENT OF ELECTRONICS ENGINEERING
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

क्रमांक: DECE/ISRO/5090 / 2024-25

दिनांक: 25/02/2025

Recruitment of Project Staff
on Purely Contract Basis

DST Sponsored Research Project: Onboard spectral preprocessing for multispectral image compression using FPGA

List of Short Listed Candidates:

Junior Research Fellow

Sr. No.	Name of candidates	Eligible	Remarks
1.		NIL	

Research Personnel (Project Associate)

Sr. No.	Name of candidates	Eligible	Remarks
1.	Bandaru Poorna Chandra	Eligible	--

1. Interview for the shortlisted candidates will be held on **28th February, 2025** from 11:30 am in Department of Electronics Engineering. **Short listed candidates must report by 10:00 am** in Department office. Candidate, who can't attend interview physically, can send request for Online Interview process through e-mail id: add@eced.svnit.ac.in with valid reasons.
2. Candidate must bring relevant original certificate, experience letter (if any) and 2 photographs at the time of interview.
3. List of selected candidates will be displayed on the institute website within one week after interview held.
4. **No TA/DA will be paid for attending the Interview.**




Dr. A. D. Darji
PI, ISRO Project