




DEPARTMENT OF ELECTRICAL ENGINEERING
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY,
SURAT

ADVERTISEMENT FOR THE POST OF THE STUDENT INTERNSHIP

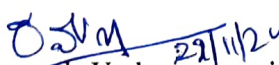
Application are invited from the indian nationals for the post of student internship under the sponsored project “**Hardware Implementation of 3SS Inverter Configuration in Solar Grid Tied Systems for Effective Energy Utilization Using Wide Bandgap Switches**” funded by ANRF (formerly SERB) in the Department of Electrical Engineering, SVNIT Surat, Gujarat as per the details below:


S.No	Name of the Post	Student Internship
1	Qualification	The applicant must have passed/ pursuing B.Tech (Electrical Engineering) or M.Tech(Electrical Engineering) or Ph.D
2	Emoluments	Rs. 5,000/- p.m
3	Date of Interview	26 th Nov 2024 (2pm onwards)
4	Mode of Interview	Hybrid Mode (offline and online)
5	Nature of Appointments	Post is purely on a contract basis
6	Tenure	02 (Two) Months (16-12-2024 to 15-02-2025)
7	Number of positions	01
8	Venue	Seminar Hall, New Department of Electrical Engineering, SVNIT, Surat



General Terms and Conditions:

- Interested candidates can apply for this post online on or before 24th Nov 2024 by submitting the Google form as per the given above QR Code and send an email to ramana@eed.svnit.ac.in.
- Please mention “**Student Internship**” in the email’s subject line. Please refer to the attached file for detailed application guidelines.
- No TA/DA will be paid for attending the interview
- The candidate is expected to join immediately, if selected.
- Preference will be given to SC/ST/OBC candidates if found suitable as per the Ministry of Education rules.
- For any further information, please contact Dr. Jammala Venkataramanaiah (PI) thorough e-mail (ramana@eed.svnit.ac.in)


Dr. Jammala Venkataramanaiah
PI, ANRF Project.


Head, DoEE,
SVNIT, Surat.