



DEPARTMENT OF ELECTRONICS ENGINEERING  
डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

क्रमांक: DoECE / Ph.D. Adm/ 1969 / 2025-26

दिनांक: 01/01/2026

**Ph. D. Admission (Academic Year-2025-26 )**

**List of Eligible/ Provisionally Eligible Candidates for the DoECE, JANUARY – 2026 Phase- II**

Sr. No.	App. ID	User Name	Candidate's Name	Ph.D. Cat.	Eligible / PE / NE
1.	12883	SVNIT3PHZZ	Manisha Gumanrao Kude	ERS (Self-Finance)	PE
2.	12887	SVNIT3P2DZ	Archana Patel	ERS (Self-Finance)	PE
3.	12943	SVNIT3PQPD	Patil Yogita Pundlikrao	ERS (Part-Time)	PE
4.	12944	SVNIT3PGTM	Songala Komal Kumar	ERS (Part-Time)	PE
5.	12970	SVNIT3PSKT	Meshram Mangesh Bhaurao	FIR	E
6.	12976	SVNIT3PTKY	A Krishna Kumar	ERS (Part-Time)	PE
7.	12981	SVNIT3P4FK	Neeta Sonwane	ERS(Part-Time)	PE
8.	12989	SVNIT3PKWI	Mankar Prasanth	ERS (Part-Time)	PE
9.	13028	SVNIT3PFWC	Tejashree Mahadev Shinde	ERS (Self-Finance)	PE


**Note: E - Eligible, PE- Provisionally Eligible, NE- Not Eligible**

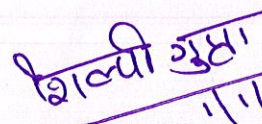
- The candidates must produce original documents with relevant **mark-sheets of all semesters** as well as **final degree certificate** of B.E./B. Tech. and M.E./M. Tech., B.Sc/M.Sc. at the time/day of the interview. Failing which they will not be allowed to participate in admission process.
- Candidates applied under OBC category must produce **recent(April-2025 onwards) NCL** in English language.
- Candidates who have applied in FIR category need to produce the **GATE/NET Score card**.
- All **PE** candidates applied under **ERS(Part Time)** category for Ph.D.Admission must enclose self-attested copies of the following documents/certificates. Same must be produced in original at the time of interview/physical documents verification.
  - Appointment letter and sponsorship certificate/no objection certificate issued by the current employer
  - Experience certificate duly signed by competent authority on the letter head of the Company/Organization/Institute.
  - The external candidates should produce fresh NOC/Sponsorship letter strictly as per format in Appendix III and VII.
  - Other documents like date of birth and identity proofs, marksheets of all semesters, degree certificates.

➤ For any queries contact [office@eced.svnit.ac.in](mailto:office@eced.svnit.ac.in)

**Instruction For Candidates:**

- The above Candidates are requested to Report at the Department of Electronics Engineering on **06/01/2026 at 9:00 AM** with **all Original Documents**. **Short-listed candidates must remain present for the interview/presentation at Room No. EC004N Conference room, Ground Floor.**
- The Presentation/ interview will **start at 11:00 AM**. Candidates are required to give a presentation as per the following guidelines.
  - The presentation should not be more than a maximum of 10 minutes duration.
  - A PowerPoint presentation is a must.
  - The maximum number of slides including the title slide should not be more than 8.
  - The presentation should contain the background of topic, literature survey, research gap, problem identification and research methodology.
- Provisionally eligible candidates will be allowed to appear only if they submit appropriate documents at the time of document verification (with a self-attested copy).

  
Dr. Z. M. Patel , Dr. J. N. Patel  
**Ph. D In-charges**

  
Dr. (Ms.) Shilpi Gupta  
**Head, DoECE**