



DEPARTMENT OF ELECTRONICS ENGINEERING
डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

क्रमांक: DoECE / Ph.D. Adm/ 1668 / 2024-25

दिनांक: 02/12/2024

Ph. D. Admission

List of Eligible/ Provisionally Eligible/ Not Eligible Candidates for the DoECE, DEC-2024

Academic Year-2024-25

Sr. No.	App. ID	User Name	Ph.D. Cat.	Eligible / Provisionally Eligible/ Not Eligible	Sr. No.	App. ID	User Name	Ph.D. Cat.	Eligible / Provisionally Eligible/ Not Eligible
1.	10270	SVNIT3PJDT	FIR	E	11.	10007	SVNIT3PBS5	ERS	PE
2.	10022	SVNIT3P2GU	FPS	E	12.	9957	SVNIT3PHQG	ERS	PE
3.	9887	SVNIT3PN3U	ERS	E	13.	10348	SVNIT3PRTB	ERS	PE
4.	9859	SVNIT3PZEA	FPS	PE	14.	10000	SVNIT3PKHZ	ERS	PE
5.	10389	SVNIT3PP7A	ERS	PE	15.	10051	SVNIT3PUAB	ERS	PE
6.	9894	SVNIT3PQAQ	ERS	PE	16.	10337	SVNIT3PMFX	ERS	NE
7.	10136	SVNIT3PONG	ERS	PE	17.	10358	SVNIT3P58C	ERS	NE
8.	10142	SVNIT3PPCF	ERS	PE	18.	10296	SVNIT3PRTL	ERS	NE
9.	10209	SVNIT3PTTI	ERS	PE	19.	9958	SVNIT3PUIC	FIR	NE
10.	10310	SVNIT3PF9A	ERS	PE	20.	10184	SVNIT3PLSZ	FPS	NE
					21.	10308	SVNIT3PUMA	FIR	NE

Note: E - Eligible, PE- Provisionally Eligible, NE- Not Eligible

- All PE candidates applied under **ERS** category for Ph.D. Admission must enclose self-attested copies of the following documents/certificates. Same must be produced in original at the time of interview/physical documents verification.
 - Appointment letter issued by the current employer.
 - Experience certificate duly signed by competent authority on the letter head of the Company/Organization/Institute.
 - Salary certificates/slips of last 12 months.
 - Income-Tax return or Form-16 of last financial year.
 - All other documents like Date of Birth and Identity Proofs, all semester marksheets, degree certificates, sponsorship or no objection certificate.

Note:

- Sponsorship Certificate should be from the same organization who has issued the Appointment letter, Experience certificate and Salary slip.
- Salary Slip, Experience Certificate of one year must be from same (single) organization.

- All PE candidates applied under **FPS** category for Ph.D. Admission must enclose self-attested copies of the all marksheets and Degree certificate of B.Tech/B.E/M. Tech/M.E. Same must be produced in original at the time of interview/physical documents verification.

➤ Please contact Dr. (Mrs.) S. Gupta (M. 97370 11973) & Dr. Z. M. Patel (M. 98986 98793) for any further information.

Instruction For Candidates

- The above Candidates are requested to Report at the Department of Electronics Engineering on **12/12/2024 at 09:30 AM with all Original Documents. Short-listed candidates must remain present for the interview/presentation at Room No. EC004N Conference room, Ground Floor.**
- The Presentation/ interview will **start at 11:00 AM**. Candidates are required to give a presentation as per the following guidelines.
 - The presentation should not be more than a maximum of 10 minutes duration.
 - A PowerPoint presentation is a must.
 - The maximum number of slides including the title slide should not be more than 8.
 - The presentation should contain the background of topic, literature survey, research gap, problem identification and research methodology.
- Provisionally eligible candidates will be allowed to appear only if they submit appropriate documents at the time of document verification (with a self-attested copy).

Dr. (Mrs.) S. Gupta

Scrutiny Committee Member

Dr. Z. M. Patel

Scrutiny Committee Member

Prof. J.N. Sarvaiya

Head, DoECE