



डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग

DEPARTMENT OF ELECTRONICS ENGINEERING

सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT

क्रमांक: DECE / 1096 / 2023-24

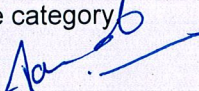
दिनांक: 11/08/2023

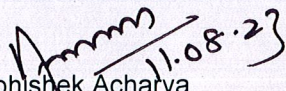
Shortlisted candidates for Admission to M. Tech. – I (CS/VL), Autumn Semester of Academic Year 2023-24 (Institute Spot Round for GATE and NON-GATE Candidates)

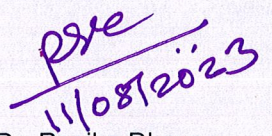
Following Candidates are Eligible/Provisionally Eligible for the Admission to M. Tech.-I (CS/VL), Autumn Semester of Academic Year 2023-24 (Institute Spot Round for GATE and NON-GATE Candidates) in the Department of the Electronics Engineering.

Sr. No.	Application No.	Name of the Candidate	GATE	Eligibility	Remarks
1.	SVNIT3MTQH	Shreyanshu Jaiswal	Yes	Eligible	--
2.	SVNIT3M7IR	Rahul Cheruku	Yes	Provisionally Eligible	B. Tech. Mark sheets of all Semesters are Not Attached
3.	SVNIT3MR8R	Yashu Sarawagi	No	Eligible	
4.	SVNIT3MTPR	Siddha Shah	No	Provisionally Eligible	Latest EWS Certificate as per CCMT 2023 not attached
5.	SVNIT3MGXH	Siddharthsingh Chandel	No	Eligible	--
6.	SVNIT3MBHI	Jehan Shukla	No	Eligible	Degree Certificate is not attached
7.	SVNIT3MF5W	Aksharasree J B	No	Provisionally Eligible	1. Result of the Final Semester is Awaited 2. Latest OBC NCL not attached
8.	SVNIT3MYCJ	Yadav Prince	No	Provisionally Eligible	Proof of Category/PWD is not attached
9.	SVNIT3MFAS	Anchala Ranjan	No	Provisionally Eligible	Latest OBC NCL not attached
10.	SVNIT3MQCN	Harshkumar Parmar	No	Provisionally Eligible	1. Caste Certificate as per CCMT 2023 Format 2. B. Tech Mark sheets Not Attached
11.	SVNIT3M7EB	Janmay Trivedi	No	Eligible	B. Tech Mark sheets Not Attached
12.	SVNIT3MIAF	Neha Tiwari	No	Eligible	B. Tech Mark sheets of all semesters are Not Attached
13.	SVNIT3MRQR	Nilay Patel	No	Eligible	--
14.	SVNIT3MEGR	Vikesh Nayak	No	Provisionally Eligible	Latest OBC NCL Certificate as per CCMT 2023 is not attached

- Candidates are requested to Report to the Department of Electronics Engineering on **17/08/2023 at 08:30 AM** with **all Original Documents**. Candidates are informed to bring one set of self-attested copies of all original documents.
- The **written test** will be conducted of **50 Marks (MCQ) a for 1-hour duration for Non-GATE candidates**. The **syllabus of the written test will be as per GATE 2023, Electronics and Communication Engineering**.
- Please note that if original documents are not in English/ Hindi, English/ Hindi version/translation of such certificates, duly certified by the Principal/Director or other competent authority of the graduating Institute will be required to be produced.
- Caste certificate (SC/ST/OBC-NCL) issued by Maharashtra State must be validated by the Social Welfare department (in case of SC and OBC-NCL category) and Tribal Welfare department (in case of ST category) in the Maharashtra Government. The SC/ST/OBC-NCL candidates of Maharashtra state have to produce their caste validity certificate in the format available on the CCMT website. ST certificate for a candidate from Tamil Nadu state must be issued by the Revenue divisional officer.
- Those who have not qualified for the GATE exam with Electronics and Communication Engineering, will be considered non-gate category.


Dr. A. S. Mandloi
PG Incharge (CS)


Dr. Abhishek Acharya
PG Incharge (VLSI)


Dr. Rasika Dhavse
Head, DECE