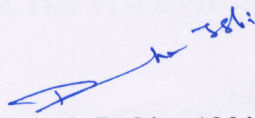
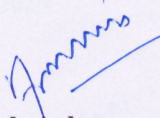
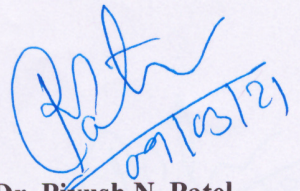


**ELECTRONICS ENGINEERING DEPARTMENT****इलेक्ट्रॉनिक्स इंजीनियरिंग डिपार्टमेंट****SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT****सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत****क्रमांक: ECED/Mid-Semester Exam/1637 / 2020-21****दिनांक: 08/03/2021****M. Tech. I (CS and VL) & PhD Mid - Semester Exam, March - 2021**

Date	Subject (Time for all 10:30 AM to 12:00 Noon)
15/03/2021	ADHOC Network (EC636)
16/03/2021	Analog VLSI Design (EC612)
17/03/2021	Testing & Verification of VLSI Circuits (EC644)
	Image Processing (EC602)
18/03/2021	Wireless Communication (EC604)
19/03/2021	VLSI System Design (EC642)
	MIMO Technology (EC638)
20/03/2021	Real Time Systems (EC614)

Venue:**M. Tech. CS & PhD – EC 201N****M. Tech. VLSI & PhD – EC 202N**


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Abhishek Acharya,
Examination Coordinators, ECED


Dr. Piyush N. Patel
HOD, ECED

All course coordinators hereby requested to mention Course Outcomes (Cos) along with questions as per accreditation requirement.

Copy to:

- 1) Dean (Acad.) for kind information
- 2) Student NB ECED,
- 3) upload@svnit.ac.in
- 4) eced_faculty@svnit.ac.in