

**ELECTRONICS ENGINEERING DEPARTMENT, SVNIT
SUPPLYMENTARY EXAM (EVEN SEMESTER) SCHEDULE**

Outward No. _____

Date: 9.02.2021

B.Tech – (1st and 2nd Semester)

Exam Date	Time (9.30 am-11.30 am)	Exam Platform	Course Coordinator	Contact No	Mode A/B* Duration if Mode B
20/02/21 SAT	Basics of Electronics Engineering (ECE109 DEF, ECE209 ABC)	Google class	Dr. Ms. S Gupta	9737011973	Mode B (one Hour)

B. Tech II – ELECTRONICS (4th Semester)

Exam Date	Time (9.30 am-11.30 am)	Exam Platform	Course Coordinator	Contact No	Mode A/B Duration if Mode B
16/02/21 TUE	Principal Of Communication (EC206)	Ms Teams	Dr. (Mrs) U. D. Dalal	98980 57727	Mode B (one hour)
17/02/21 WED	Electrical Technology (EE208)	Ms Teams	Dr. V. P. Kumar	9160919516	Mode A
19/02/21 FRI	Control System (EE216)	Ms Teams	Dr. R. Chilipi	8247208518	Mode B (one hour)

A.S. Mandloi
Dr. A.S. Mandloi

Departmental Exam Coordinator



P.N. Patel
09/02/21
Dr. P.N. Patel
Head ECED

NOTE:

- 1) Online Backlog (even semester supplementary) exam has to be conducted as per 04th resolution of 47th special senate meeting minutes dated 20.5.2020. The weightage of mid semester exam will be of 70 marks (Mid semester = 50 marks + internal = 20 marks) Thus in theory paper of 100 marks weightage of mid semester+ internal will be = 70 marks and **end semester = 30 marks.** The actual marks secured in Tutorials would be considered as per prevailing norms in the theory subject wherever tutorial is involved. The criteria for securing 30 % marks in a theory paper during End Semester examination will be waived off for current semester for final year students only.
- 2) * Resolution 48.9 , P no 4 Of 48th Meeting of The Senate.
Mode A: Open Book Exam
Mode B: MCQ

Copy to:

- 1) Dean (Acad.) for kind information,
- 2) ECED student notice board,
- 3) Student NB EcED,
- (4) upload@svnit.ac.in
- (5) eced_faculty@svnit.ac.in
- 6) All concerned Departments

Electronics Engineering Department SVNIT, SURAT-7.
INWARD No.
OUTWARD No. <u>1419</u>
Date. <u>09/02/2021</u>