

**TIME TABLE**

Semester: ODD

Year: 2024-2025

Class: M. TECH-I (CS & VLSI)

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30 - 9.20	9.30 - 10.20	10.30 - 11.20	11.30 - 12.20	12.20 - 2.00	2.00 - 2.50	3.00 - 3.50	4.00 - 4.50	5.00 - 5.50
<b>Monday</b>	A1 (SIT)	B2 (RFCS)	C3 (DVD)	D4 (ADSP)		G5(DSC) (T&V)	H6 (ES)		
<b>Tuesday</b>	B1(DSC) (T&V)	C2 (ADSP)	D3(AOCS) (MOS)	E4 (ITC)					
<b>Wednesday</b>	C1 (SIT)	D2 (ES)	E3(AOCS) (MOS)	A4 (DVD)		J6(RFCS)			
<b>Thursday</b>	D1 (DSC)	E2(AOCS)	A3 (ITC)			<b>P15</b>	<b>P16</b>		
<b>Friday</b>	E1 (SIT)	A2 (ITC)	B3 (ADSP)	C4(RFCS) (MOS)		<b>P17</b>	<b>P18</b>		
						K5 (ES)	G6 (T&V)	I7 (DVD)	

Sl. No.	SLOT	COURSE NO	TITLE	FACULTY	CAT.	CREDIT	CLASS ROOM
(1)	B2,J6,C4	ECCS101	RF Circuits & Systems	GS,GS,GS	3-0-0	3	EC222N
(2)	D3,E2,E3	ECCS103	Advance Optical Communication Systems	PNP,PNP,PNP	3-0-0	3	EC303N
(3)	D4,C2,B3	ECCS105	Advance DSP	PKS,SD,PKS	3-0-0	3	EC222N
(4)	E4,A3,A2	ECCS111	Information Theory & Coding	JNS,JNS,JNS	3-0-0	3	EC222N
(5)	D1,B1,G5	ECCS113	Digital Satellite Communication	ASM,ASM,ASM	3-0-0	3	EC303N
(6)	C3,A4,I7	ECVL101	Digital VLSI Design	PD,PD,PD	3-0-0	3	EC222N
(7)	D3,E3 C4	ECVL103	MOS Devices and Technology	DJ,DJ,DJ	3-0-0	3	EC222N EC201N
(8)	H6,D2,K5	ECVL105	Embedded Systems	PJE,PJE,PJE	3-0-0	3	EC222N
(9)	A1,E1,C1	ECVL111	Semiconductor IC Technology	VG,VG,VG	3-0-0	3	EC222N
(10)	G5,G6,B1	ECVL117	Testing and Verification of VLSI Circuits	DJ,DJ,DJ	3-0-0	3	EC222N
(11)	P16	ECCS101	RF Circuits & Systems	GS,P24	3-0-0	3	EC403N
(12)	P18	ECCS103	Advance Optical Communication Systems	PNP,P10	3-0-0	3	EC403N
(13)	P17	ECCS105	Advance DSP	PKS,P19	3-0-0	3	EC307N
(14)	P18	ECVL101	Digital VLSI Design	PD,P17	3-0-0	3	EC306N
(15)	P16	ECVL103	MOS Devices and Technology	VG,P17	3-0-0	3	EC306N
(16)	P15	ECVL105	Embedded Systems	PJE,P11	3-0-0	3	EC307N

