

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
ELECTRICAL ENGINEERING DEPARTMENT
TIME TABLE OF M.TECH I (Power Electronics & Electrical Drives)

SEMESTER – ODD (JULY-DEC, 2021)

SESSION – 2021-22

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1	B2	C3	D4	LUNCH	P11		P12	
Tuesday	B1	C2	D3	E4		P13		P14	
Wednesday	C1	D2	E3	A4		P15		P16	
Thursday	D1	E2	A3	B4		P17		P18	
Friday	E1	A2	B3	C4		P19		P20	

SL. NO	SLOT	COURSE NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASS ROOM
1.	C1, C4	EL601	Power Electronics-I	RC	PG	4	
2.	A1, A3	EL601	Power Electronics-I	MAM	PG		
3.	P15	EL601	Power Electronics-I Lab	MAM	PG	1	
4.	B1-B3	EL 605	Digital Signal Processing	PK	PG	3	
5.	P20	EL605	Digital Signal Processing Lab	PK	PG	1	
4.	A2, B4	EL 613	Power Quality issues and Management	MA	Elective	2	
5.	P19	EL613	Power Quality Issues and Management Lab	MA		1	
4.	C2-C4	EL 607	Fundamentals of System Theory	RR	PG	3	
7.	D1, D2	EL603	Modelling of Electrical Machines & DC drives	AC	PG	4	
8.	D3, D4	EL603	Modelling of Electrical Machines & DC drives	SRA	PG		
9.	P11	EL603	Modelling of Electrical Machines & DC drives Lab	SRA	PG	1	
10.	E1	EL 617	Microcontroller Based System Design	VAS	Elective-I	2	
12.	E3, E4	EL 617	Microcontroller Based System Design	JJP		2	
13.	P19	EL617	Microcontroller Based System Design Lab	JJP, CPG		1	

K. M. Maurya
23.07.2021

(Chairman, Time Table Committee, EED)

R. Chaudhary
23/7/2021

(HoD, EED)

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
ELECTRICAL ENGINEERING DEPARTMENT
TIME TABLE OF M.TECH I (Power System Engineering)

SEMESTER – ODD (JULY-2021)

SESSION – 2021-22

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1	B2	C3	D4	LUNCH	P11		P12	
Tuesday	B1	C2	D3	E4		P13		P14	
Wednesday	C1	D2	E3	A4		P15		P16	
Thursday	D1	E2	A3	B4		P17		P18	
Friday	E1	A2	B3	C4		P19		P20	

SL. NO	SLOT	COURSE NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASSROOM
1.	A1, A2, E2	EL625	Restructuring of Power System	VM	PG	3	
2.	A4, C2	EL621	Power System Protection	HRJ	PG		
3.	B4, C3	EL621	Power System Protection	VAS	PG		
4.	P11	EL621	Power System Protection Lab	HRJ	PG	1	
5.	B1-B3	EL 631	Digital Signal Processing	PK	PG	3	
6.	D	EL 619	CA Power System Analysis	PBD	PG	4	
7.	P13	EL619	CA PSA Lab	PBD	PG	1	
8.	A3, C1, C4	EL623	Power Electronics	MDG	PG	3	
9.	P17	EL623	Power Electronics Lab	MDG, SH	PG	1	
10.	E1	EL 617	Microcontroller Based System Design	VAS	Elective-I	2	
11.	E3, E4	EL 617	Microcontroller Based System Design	JJP	Elective-I	2	

K. M. Maurya
23.07.2021

(Chairman, Time Table Committee, EED)

R. Choudhary
23/7/2021

(HoD, EED)

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
ELECTRICAL ENGINEERING DEPARTMENT
TIME TABLE OF M.TECH I (Instrumentation and Control)

SEMESTER – ODD (JUL-DEC-2021)					SESSION – 2021-22				
Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1	B2	C3	D4	LUNCH	P11		P12	
Tuesday	B1	C2	D3	E4		P13		P14	
Wednesday	C1	D2	E3	A4		P15		P16	
Thursday	D1	E2	A3	B4		P17		P18	
Friday	E1	A2	B3	C4		P19		P20	

SL. NO	SLOT	COURSE NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASSROOM
1.	C1-C3	ELIC1C03	Industrial Automation	HGP	PG	3	
2.	P13	ELIC1C03	Industrial Automation Lab	HGP	PG	1	
3.	A1-A3	ELIC1C01	Process Control	RR	PG	3	
4.	B1-B3	ELIC1C02	Modern Instrumentation	MNB	PG	3	
5.	P12	ELIC1C02	Modern Instrumentation Lab	MNB	PG	1	
6.	A4, C4	ELIC1C04	System Theory	SNS	PG	4	
7.	B4, E4	ELIC1C04	System Theory	SG	PG	4	
8.	E2	ELIC1CE01	Digital Signal Processing	CGP	Elective	3	
9.	E1, E3	ELIC1CE01	Digital Signal Processing	RC			
10.	E1-E3	ELIC1CE04	Optimal and Adaptive Control	SG			
11.	P18, P20	ELIC1C05	Advanced Instrumentation Laboratory	RR, SG	PG	2	

K. M. Mawya
23.07.2021

(Chairman, Time Table Committee, EED)

R. Chandan
23/7/2021

(HoD, EED)