



DEPARTMENT OF ELECTRONICS ENGINEERING
डिपार्टमेंट ऑफ इलेक्ट्रॉनिक्स इंजीनियरिंग
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY, SURAT
सरदार वल्लभभाई नेशनल इंस्टिट्यूट ऑफ टेक्नोलॉजी, सूरत

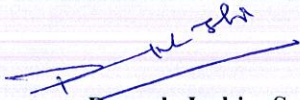
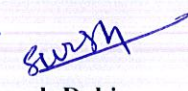
क्रमांक: DECE/Mid-Sem /Autumn/111/2020-21


दिनांक: 13/10/2021

Mid Semester Exam Schedule October 2021 [Revised]

M. Tech. I Semester (CS and VL) / Ph. D.

Date	Subject (10:30 AM to 12:00 Noon)	Course Coordinator
18/10/2021	Advance Optical Communication Systems (EC603)	Abhilash S Mandloi (7016786449)
	MOS Devices & Technology (EC611)	Shivendra Yadav (9479334046)
19/10/2021	Embedded System (EC613)	Pinal J Engineer (9924711335)
20/10/2021	Advance Digital Signal Processing (EC605)	Suman Deb (8011870732)
21/10/2021	Digital VLSI Design (EC609)	Anand D Darji (9426886446)
22/10/2021	RF circuit & system (EC601)	Golak Santra (8980692441)
23/10/2021	Information Theory & Coding (EC619)	Jignesh N Sarvaiya (9924728285)



Deepak Joshi Suresh Dahiya
Examination Coordinators, DECE


13/10/21
Dr. Piyush N. Patel
HOD, DECE

Copy to:

- 1) Dean (Acad.) for kind information
- 2) Chief Exam Coordinator
- 3) Student NB DECE
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फोन नं: संस्थान कार्यालय: २२२३३७१-७४, फैक्स नं: २२२८३९४, २२२७३३४ विभागीय प्रमुख: २२०१५५१, विभाग कार्यालय:
२२०१५५२ ई-मेल: director@svnit.ac.in, hod@eced.svnit.ac.in