

**SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY**  
Ichchhanath, Surat-395007 (Gujarat), India

**REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I SECOND SEMESTER VLSI AND EMBEDDED SYSTEMS AT REGULAR EXAM HELD DURING APRIL/MAY 2018**

					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark		
Semester	Student Name	Course Code			EC614 T	EC612 T	EC616 P	EC604 T	EC624 T	EC642 T											
					<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>											
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	WIRELESS COMMUNICATION (ELECTIVE)	NANOELECTRONICS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)											
Semester : 2 VIVEK DUBEY					CC	BB	BB	BC	BC	BC											
<b>P17VL001</b>   <b>19.00</b>   <b>19.00</b>   <b>7.21</b>   <b>6.89</b>																					
Semester : 2 SUMAN PANDIT					BB	AB	BB	BB	AB	BC											
<b>P17VL002</b>   <b>19.00</b>   <b>19.00</b>   <b>8.16</b>   <b>8.00</b>																					
Semester	Student Name	Course Code			EC614 T	EC612 T	EC616 P	EC602 T	EC624 T	EC642 T											
					<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>											
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	IMAGE PROCESSING (ELECTIVE)	NANOELECTRONICS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)											
Semester : 2 JAWALE SHUBHANGI CHANBAS					AA	BB	BB	BC	AB	BB											
<b>P17VL003</b>   <b>19.00</b>   <b>19.00</b>   <b>8.32</b>   <b>8.00</b>																					
Semester : 2 KORADA ADITYA					AA	BB	AB	BC	AB	BB											
<b>P17VL004</b>   <b>19.00</b>   <b>19.00</b>   <b>8.53</b>   <b>8.37</b>																					
Semester	Student Name	Course Code			EC614 T	EC612 T	EC616 P	EC604 T	EC624 T	EC642 T											
					<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>											
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	WIRELESS COMMUNICATION (ELECTIVE)	NANOELECTRONICS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)											
Semester : 2 BARWA NEHA NAYANKUMAR					AA	AA	AB	BC	AA	AB											
<b>P17VL005</b>   <b>19.00</b>   <b>19.00</b>   <b>9.16</b>   <b>8.84</b>																					
Semester : 2 DEEPAK KUMAR					AB	BB	BB	BB	BB	BC											
<b>P17VL006</b>   <b>19.00</b>   <b>19.00</b>   <b>8.00</b>   <b>7.95</b>																					
Semester : 2 SUDHANSHU KUMAR					II	II	BC	II	II	II											<b>BACKLOG : 15 CREDIT(S)</b>
<b>P17VL007</b>   <b>19.00</b>   <b>4.00</b>   <b>1.47</b>   <b>6.74</b>																					
Semester	Student Name	Course Code			EC614 T	EC612 T	EC616 P	EC602 T	EC624 T	EC642 T											
					<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>											
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	IMAGE PROCESSING (ELECTIVE)	NANOELECTRONICS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)											
Semester : 2 GAGARE NAMRATA BHAI SHALAN					AA	AB	BB	AA	BC	AB											
<b>P17VL008</b>   <b>19.00</b>   <b>19.00</b>   <b>8.79</b>   <b>8.71</b>																					

Abbreviations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10 AB-9 BB-8 BC-7 CC-6 CD-5 DD-4 FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFACTORY ATTENDANCE

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					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark			
Semester	Student Name	Course Code			EC614 T	EC612 T	EC616 P	EC604 T	EC624 T	EC642 T												
					<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>												
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	WIRELESS COMMUNICATION (ELECTIVE)	NANOELECTRONICS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)												
Semester : 2 SARIPALLI GAYATRI					AB	BB	AB	BC	BB	BC												
<b>P17VL009</b>   <b>19.00</b>   <b>19.00</b>   <b>8.05</b>   <b>8.05</b>																						
Semester : 2 BOBBILI SAGAR					BB	BC	BB	CC	BC	DD												
<b>P17VL010</b>   <b>19.00</b>   <b>19.00</b>   <b>6.74</b>   <b>6.39</b>																						
Semester : 2 AKANKSHA SINGH					AB	AB	BB	BB	BB	BB												
<b>P17VL011</b>   <b>19.00</b>   <b>19.00</b>   <b>8.32</b>   <b>7.84</b>																						
Semester : 2 RAJOO KUMAR GUPTA					AA	AB	AB	BB	BC	BC												
<b>P17VL012</b>   <b>19.00</b>   <b>19.00</b>   <b>8.37</b>   <b>7.97</b>																						
Semester : 2 CHANDRAPRAKASH SUWALAKA					BB	CD	BB	BC	BC	CD												
<b>P17VL014</b>   <b>19.00</b>   <b>19.00</b>   <b>6.74</b>   <b>6.50</b>																						
Semester : 2 RATNADEEP PATIL					CD	DD	BC	FF	CC	CC											BACKLOG : 3 CREDIT(S)	
<b>P17VL015</b>   <b>19.00</b>   <b>16.00</b>   <b>4.79</b>   <b>5.29</b>																						
Semester	Student Name	Course Code			EC614 T	EC612 T	EC616 P	EC602 T	EC624 T	EC642 T												
					<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>												
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	IMAGE PROCESSING (ELECTIVE)	NANOELECTRONICS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)												
Semester : 2 PRAVEEN					AB	BC	AB	DD	BB	BC												
<b>P17VL016</b>   <b>19.00</b>   <b>19.00</b>   <b>7.42</b>   <b>6.95</b>																						
Semester : 2 VISHWAJEET S B					CC	BC	AB	CC	BC	BB												
<b>P17VL017</b>   <b>19.00</b>   <b>19.00</b>   <b>7.26</b>   <b>7.47</b>																						
Semester	Student Name	Course Code			EC614 T	EC612 T	EC616 P	EC604 T	EC624 T	EC642 T												
					<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>												
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	WIRELESS COMMUNICATION (ELECTIVE)	NANOELECTRONICS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)												
Semester : 2 RAJESH KUMAR					BC	CD	BC	CD	DD	DD												
<b>P17VL018</b>   <b>19.00</b>   <b>19.00</b>   <b>5.42</b>   <b>5.42</b>																						
Semester	Student Name	Course Code			EC614 T	EC612 T	EC616 P	EC602 T	EC624 T	EC642 T												
					<b>3.00</b>	<b>3.00</b>	<b>4.00</b>	<b>3.00</b>	<b>3.00</b>	<b>3.00</b>												
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	REAL TIME SYSTEMS	ANALOG VLSI DESIGN	VLSI LABORATORY - II	IMAGE PROCESSING (ELECTIVE)	NANOELECTRONICS (ELECTIVE)	VLSI SYSTEM DESIGN (ELECTIVE)												
Semester : 2 WILFRED KISKU					AB	CC	AB	FF	BB	CC												BACKLOG : 3 CREDIT(S)
<b>P17VL019</b>   <b>19.00</b>   <b>16.00</b>   <b>6.47</b>   <b>7.03</b>																						

Total Student Count : 18

Abbreviations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10 AB-9 BB-8 BC-7 CC-6 CD-5 DD-4 FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFACTORY ATTENDANCE

Date : 14-06-2018

The actual mark statement with the Academic / Examination section shall be final & binding to all