



DEPARTMENT OF ELECTRONICS ENGINEERING
SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY,
SURAT
 ICHCHHANATH, SURAT-395007 (GUJARAT)
 website: www.svnit.ac.in

Shortlisted candidates for the post of Lab Engineer under SMDP-C2SD project

SR. NO.	NAME OF CANDIDATE
1	BHUSHRA PRAVEEN
2	KIRAN SHAMRAO PATIL
3	AMIYA BISOI
4	HETALI AMRUTBHAI PATEL
5	UPASANA NEOGI
6	MEHUL RAJESH GAJJAR
7	AKSH RAJNIKANT PATEL
8	GURRAM MAHESH
9	BHAKTI PARMAR
10	AVIJIT PATRA
11	DHRUTI BABUBHAI PATEL
12	ANAND CHAUDHARI
13	DEEPTHY XAVIER
14	DHANYA XAVIER
15	SAGAR RAMESH AAKRE



A. D. Darji
 (Dr. A. D. Darji)
 CI, SMDP - C2SD Project
 SVNIT Surat.

Dr. A. D. Darji
 Head
 Electronics Engg. Deptt.

Notes:

1. Interview/Test for the shortlisted candidates will be held on **13th April, 2018** at **11.00 AM** in **New Electronics Engineering Department, SVNIT, Surat.**
2. **Candidates have to report at ECED office, New Electronics Engineering Department, SVNIT, Surat on or before 10:00 AM.**
3. Candidates have to appear for interview with **Bio-Data, 2 Passport size Photos, Duly filled application form, Original Certificates and one set of self-attested photocopies.**
4. An applicant has to ensure the authenticity of information provided in support of experience claimed, other documents and photograph.
5. **No TA/DA will be paid for appearing in the Interview.**
6. Candidate employed in institute/Industry must produce No-Objection Certificate (NOC) at the time of interview.