


Electronics Engineering  
SVNIT, SURAT

INWARD No. ....

OUTWARD No. 44

Date 10/4/18



**DEPARTMENT OF ELECTRONICS ENGINEERING**  
**SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY,**  
**SURAT**  
 ICHCHHANATH, SURAT-395007 (GUJARAT)  
 website: [www.svnit.ac.in](http://www.svnit.ac.in)

**Shortlisted candidates for the post of Project/Research Associate under SMDP-C2SD project**

SR. NO.	NAME OF CANDIDATE
1	DHRUTI BABUBHAI PATEL
2	TRUPTI PRAVINBHAI PATEL
3	HIRENKUMAR MANJIBHAI PANSURIYA
4	BHAKTI PARMAR
5	ASHISH CHAND SINGH
6	GAURAV MISHRA
7	AVINASH SUKADEO PAWAR
8	AKSH RAJNIKANT PATEL
9	MALA LAKHWANI
10	AMIYA BISOI
11	KIRAN SHAMRAO PATIL



*Anand*  
 (Dr. A. D. Darji)  
 CI, SMDP - C2SD Project  
 SVNIT Surat.

*Am*  
 Head  
 Electronics Engg. Deptt.

**Notes:**

1. Interview/Test for the shortlisted candidates will be held on **13<sup>th</sup> April, 2018** at **11.00 AM** in **New Electronics Engineering Department, SVNIT, Surat.**
2. **Candidates have to report at ECED office, New Electronics Engineering Department, SVNIT, Surat on or before 10:00 Am.**
3. Candidates have to appear for interview with **Bio-Data, 2 Passport size Photos, Duly filled application form, Original Certificates and one set of self-attested photocopies.**
4. An applicant has to ensure the authenticity of information provided in support of experience claimed, other documents and photograph.
5. **No TA/DA will be paid for appearing in the Interview.**
6. Candidate employed in institute/Industry must produce No-Objection Certificate (NOC) at the time of interview.