

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY
Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I FIRST SEMESTER VLSI AND EMBEDDED SYSTEMS AT REGULAR EXAM HELD DURING NOV/DEC 2017

					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark		
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC605 T	EC623 T											
					3.00	3.00	3.00	4.00	3.00	3.00											
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)	HARDWARE DESCRIPTION LANGUAGE (ELECTIVE)											
Semester : 1 VIVEK DUBEY					BC	BC	CC	BB	BC	DD											
P17VL001 19.00 19.00 6.58 6.58																					
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC619 T	EC623 T											
					3.00	3.00	3.00	4.00	3.00	3.00											
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	INFORMATION THEORY & CODING (ELECTIVE)	HARDWARE DESCRIPTION LANGUAGE (ELECTIVE)											
Semester : 1 SUMAN PANDIT					BC	AB	BB	BB	BB	BC											
P17VL002 19.00 19.00 7.84 7.84																					
Semester : 1 JAWALE SHUBHANGI CHANBAS					BC	BB	AB	BB	BC	BC											
P17VL003 19.00 19.00 7.68 7.68																					
Semester : 1 KORADA ADITYA					BC	BC	AA	AB	BB	BB											
P17VL004 19.00 19.00 8.21 8.21																					
Semester : 1 BARWA NEHA NAYANKUMAR					BB	AB	AB	AB	BB	BB											
P17VL005 19.00 19.00 8.53 8.53																					
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC605 T	EC623 T											
					3.00	3.00	3.00	4.00	3.00	3.00											
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)	HARDWARE DESCRIPTION LANGUAGE (ELECTIVE)											
Semester : 1 DEEPAK KUMAR					BB	BB	BC	AB	BB	BC											
P17VL006 19.00 19.00 7.89 7.89																					
Semester : 1 SUDHANSHU KUMAR					BC	BC	BC	BC	BB	DD											
P17VL007 19.00 19.00 6.68 6.68																					
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC619 T	EC623 T											
					3.00	3.00	3.00	4.00	3.00	3.00											
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	INFORMATION THEORY & CODING (ELECTIVE)	HARDWARE DESCRIPTION LANGUAGE (ELECTIVE)											
Semester : 1 GAGARE NAMRATA BHAU SHALAN					BB	BB	AA	BB	AB	AB											
P17VL008 19.00 19.00 8.63 8.63																					
Semester : 1 SARIPALLI GAYATRI					CC	AB	AB	AB	BC	BB											
P17VL009 19.00 19.00 8.05 8.05																					

Abbreviations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10 AB-9 BB-8 BC-7 CC-6 CD-5 DD-4 FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFACTORY ATTENDANCE

The actual mark statement with the Academic / Examination section shall be final & binding to all

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					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark			
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC605 T	EC623 T												
					3.00	3.00	3.00	4.00	3.00	3.00												
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)	HARDWARE DESCRIPTION LANGUAGE (ELECTIVE)												
Semester : 1 BOBBILI SAGAR					DD	CC	BC	BC	BB	DD												
P17VL010 19.00 19.00 6.05 6.05																						
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC619 T	EC623 T												
					3.00	3.00	3.00	4.00	3.00	3.00												
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	INFORMATION THEORY & CODING (ELECTIVE)	HARDWARE DESCRIPTION LANGUAGE (ELECTIVE)												
Semester : 1 AKANKSHA SINGH					CC	BB	BB	BB	BB	CC												
P17VL011 19.00 19.00 7.37 7.37																						
Semester	Student Name	Course Code			EC609 T	EC611 T	EC613 T	EC615 P	EC605 T	EC623 T												
					3.00	3.00	3.00	4.00	3.00	3.00												
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LABORATORY-I	ADVANCE DIGITAL SIGNAL PROCESSING (ELECTIVE)	HARDWARE DESCRIPTION LANGUAGE (ELECTIVE)												
Semester : 1 RAJOO KUMAR GUPTA					BC	BC	CC	AB	AB	BC												
P17VL012 19.00 19.00 7.58 7.58																						
Semester : 1 CHANDRAPRAKASH SUWALAKA					CC	CC	CD	BB	BB	DD												
P17VL014 19.00 19.00 6.26 6.26																						
Semester : 1 RATNADEEP PATIL					DD	FF	DD	BC	FF	DD											BACKLOG : 6 CREDIT(S)	
P17VL015 19.00 13.00 3.37 4.92																						
Semester : 1 PRAVEEN					CC	CD	CD	AB	CC	BC												
P17VL016 19.00 19.00 6.47 6.47																						
Semester : 1 VISHWAJEET S B					BB	BB	CC	BB	BC	AB												
P17VL017 19.00 19.00 7.68 7.68																						
Semester : 1 RAJESH KUMAR					DD	CD	CD	BC	BC	DD												
P17VL018 19.00 19.00 5.42 5.42																						
Semester : 1 WILFRED KISKU					DD	BC	FF	AB	CC	CD												BACKLOG : 3 CREDIT(S)
P17VL019 19.00 16.00 5.37 6.38																						

Total Student Count : 18

Abbreviations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10 AB-9 BB-8 BC-7 CC-6 CD-5 DD-4 FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFACTORY ATTENDANCE

Date : 09-01-2018

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