

PROGRAMME OBJECTIVE

The Department of Computer Engineering, S. V. National Institute of Technology is organizing one week short Term Training Programme (STTP) on "HIGH PERFORMANCE COMPUTING (HPC-2019)" during 27th May – 31st May, 2019 under TEQP-III, in collaboration with CDAC, Pune under National Supercomputing Mission (NSM). This STTP is aimed to communicate new programmers and scientists about the basics of High Performance Computing (HPC). Too many parallel and high performance computing material focus on the architecture, theory and computer science surrounding HPC. This program emphasis on the faculties, researchers as well as students from multidisciplinary departments who need to write and run their programs as part of their research. HPC provides the best solution to solve the challenge of real time processing by utilizing massive parallelism. HPC have the potential to resolve the real time and complex problem accurately, effectively and efficiently

Objectives:

- HPC impact
- Insights into HPC systems
- Recent Trends in HPC
- Research and career opportunities in HPC
- Parallel programming paradigm
- Deep Learning paradigm
- Demonstration/Hands-on on HPC environment and application

SPEAKERS

Eminent researchers and speakers from C-DAC Pune as well as Faculties from Host Institution to shape up the knowledge programming for teachers/ researchers/ scientists.

PROFORMA FOR REGISTRATION FORM

Name: (In Block Letters) _____

Gender: _____

Designation: _____

Institutional Address: _____

Highest Qualification: _____

Area of Specialization: _____

Research Interests: _____

Address for Correspondence: _____

Tele/Fax & Mobile No. _____

Email: _____

Demand Draft No: _____ drawn on

_____ dated _____

Payable at Surat

Date: _____ Signature of Applicant

The candidate

Mr/Mrs/Dr/Prof _____

_____ is hereby officially permitted by this institute to attend the STTP titled "HIGH PERFORMANCE COMPUTING, 2019 (HPC-2019)" during the period 27th May – 31st May, 2019 at SVNIT Surat.

DIRECTOR/PRINCIPAL with SEAL

One Week Short Term Training Programme (STTP)

Under TEQP-III

In Collaboration with CDAC, Pune

(Under NSM)

On

HIGH PERFORMANCE COMPUTING 2019 (HPC-2019)

27th May – 31st May, 2019

Coordinators

Dr. Rupa G. Mehta

Dr. Dipti P. Rana



Organized By

**Computer Engineering Department
Sardar Vallabhbhai National Institute
of Technology (SVNIT)
Ichhanath, Surat-395007, Gujarat, India**

Ph. +91 261 2201596, 2201589

Email: rgm@coed.svnit.ac.in, dpr@coed.svnit.ac.in

ABOUT THE INSTITUTE AND SURAT

The institute, one of the pioneering engineering institutions of the country, was established in 1961 as Sardar Vallabhbhai Regional College of Engineering & Technology and was given a status of National Institute of Technology, in 2002. At present, there are six undergraduate courses, seventeen postgraduate courses and Ph.D. programmes in all disciplines of engineering and applied sciences. It has an excellent placement record with a number of top ranking companies visiting the campus. The whole campus has connectivity of the internet with fiber optic network including faculty quarters and student hostels.

The institute is located at Surat, about 260 kms North of Mumbai and is very well connected by rail and road links to Mumbai as well as Ahmedabad (250 kms)/Vadodara (150 kms). The institute is approximately 10 kms away from Surat Railway Station and 10 kms from the Surat Airport (STV). Surat is the industrial city with historical importance and is well known for Textile, Jari and Diamond industries. The leading industries like RIL, ONGC, Kribhco, L&T, ESSAR, NTPC, and GAIL are established in Surat- Hazira area.

ABOUT THE DEPARTMENT

The department is running UG programme B.Tech. Computer Engineering since 1989 and PG programme M.Tech. Computer Engineering since 2006. Department is also offering Ph.D. programme since 2005. The department has well qualified and dedicated faculty members with the specialization in niche areas like information security, Cloud computing, operating systems, image processing, Data Mining, wireless sensor network, computer vision etc.

PATRON

Prof. S. R. Gandhi
Director, SVNIT

COORDINATORS

Prof. R. G. Mehta
Dr. D. P. Rana

DEPARTMENT HEAD

Prof. M. A. Zaveri

LOCAL TECHNICAL COMMITTEE

Prof. R. P. Gohil
Dr. K. N. Jariwala
Dr. U. P. Rao
Dr. S. J. Patel
Mr. B. Tidke
Mr. J. Dhanani
Ms. M. Desai
Mr. M. Makwana
Mr. J. Vanara
Mr A. Makwana

OFFICIAL CORRESPONDENCE ADDRESS

The Coordinators,
High Performance Computing (HPC-2019)
Computer Engineering Department,
S.V. National Institute of Technology,
Ichchhanath, Surat – 395 007, Gujarat, India.
Ph. +91 261 2201596, 2201589
Email: rgm@coed.svnit.ac.in,
dpr@coed.svnit.ac.in

ELIGIBILITY

Students/Faculty Members from academic institutes, universities, research scholars and industry personnel working in the concerned /allied discipline.

IMPORTANT DATES

Last date for Receiving : 15th May, 2019
Registration with DD

List of Eligible Candidates : 20th May, 2019
on Institute website
(separate mail will be sent)

REGISTRATION FEES

REFUNDABLE registration fee of Rs. 1000/- per participant for students, Academicians, Scientist, Researchers, Practicing Engineers and Professionals.

Link for online registration:
<https://forms.gle/dFgzT8KwqPZerFtF9>

After filling online registration, it is **necessary** to send the Registration Form and DD (payable at Surat) drawn in the favour of “**The Director, SVNIT**”, to the official address for correspondence on or before 15th May, 2019.

TRAVEL

The participants have to bear their own travelling expenses.

ACCOMODATION

The participants will be provided free accommodation in institute premises as per institute norms.

A course-kit as well as tea/coffee and working lunch & dinner will be served during the period of the STTP.