

M. Tech Colloquium

in

Information Security and Privacy

Under the ISEA II Project

Mar 17, 2018

Organized By



**Department of Computer Engineering
S. V. National Institute of Technology,**

Surat – 395007, Gujarat, India

Ph. +91 261 2223371-74, 2201592

Fax. +91 261 2228394

Email: sjp@coed.svnit.ac.in

dcj@svnit.ac.in

Website : <http://www.svnit.ac.in>

Sardar Vallabhbhai National Institute of Technology, Surat popularly known as NIT Surat is one of the pioneering engineering institutions of the country, was established in 1961 as Sardar Vallabhbhai Regional College of Engineering & Technology. It is recognized by government of India as an Institute of National Importance. The institute offers Undergraduate, Postgraduate & Doctoral courses in the field of Engineering & Technology, Science, Humanities & Management. The Department of Computer Engineering established in 1989 with vision of recognizing itself as renowned department in the field of technical education and research field. The department has qualified and dedicated faculty members with the specialization in various areas like information security, operating systems, image processing, wireless network, computer vision, machine learning etc.

The **M.Tech. Colloquium in Information Security and Privacy 2018** to be organized as a part of ISEA II Project is a platform for students to present their ideas to the research community and to interact closely with experts. The students will gain fresh perception on research topics that will not only help to strengthen their research knowledge but it will also leave positive impact on their work as well as their career.

The area of Research – not necessarily must be confined to those in Information Security and Privacy. A few indicative ones are as follows:

- Privacy-enhancing technologies
- Cyber, network and communication security
- Social network information
privacy/security/integrity
- Secure computing and systems
- Information integrity structure and design
- Access control
- Authentication, Authorization and
Accountability
- Biometric Security
- Cryptography
- Data and System Integrity
- Grid Security
- Information Hiding and Watermarking
- Intrusion Detection
- Key Management and Key Recovery
- Security Models
- Security Protocols
- Signature and Key Agreement Protocol

Eligibility

- Final year M.Tech students from any AICTE approved engineering colleges/universities

How to participate?

- Interested participants have to submit abstract(Maximum two pages) of their M.Tech Dissertation and his/her details as specified in the format given at <http://svnit.ac.in/MTechColloquium2018.pdf>
- Selected participants need to present their work in the form of poster.
- The top three participants will be rewarded

Submission Deadlines

Last date for submission of abstracts	Mar 12, 2018
Confirmation of acceptance of abstracts	Mar 13, 2018
Last date of registration for participation	Mar 14, 2018
Colloquium Date	Mar 17, 2018

NOTE: There is no fee to participate in this colloquium.

Travel assistance WILL NOT be provided by Institute, however hostel accommodation may be provided subject to the availability on chargeable basis in SVNIT hostels.

Colloquium Coordinators

Dr. Sankita J Patel, Assistant Professor
Prof. D C Jinwala, Professor,
Computer Engineering Department,
SVNIT, Surat- 395007,
Gujarat, India

**Subject to approval by the Institute